



# UT-0622-P03

## Evaluation Kit User Guide

This document Suit below Products' EVK

|                |  |
|----------------|--|
| UG-5664GSWHG01 |  |
|                |  |
|                |  |

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Version : A



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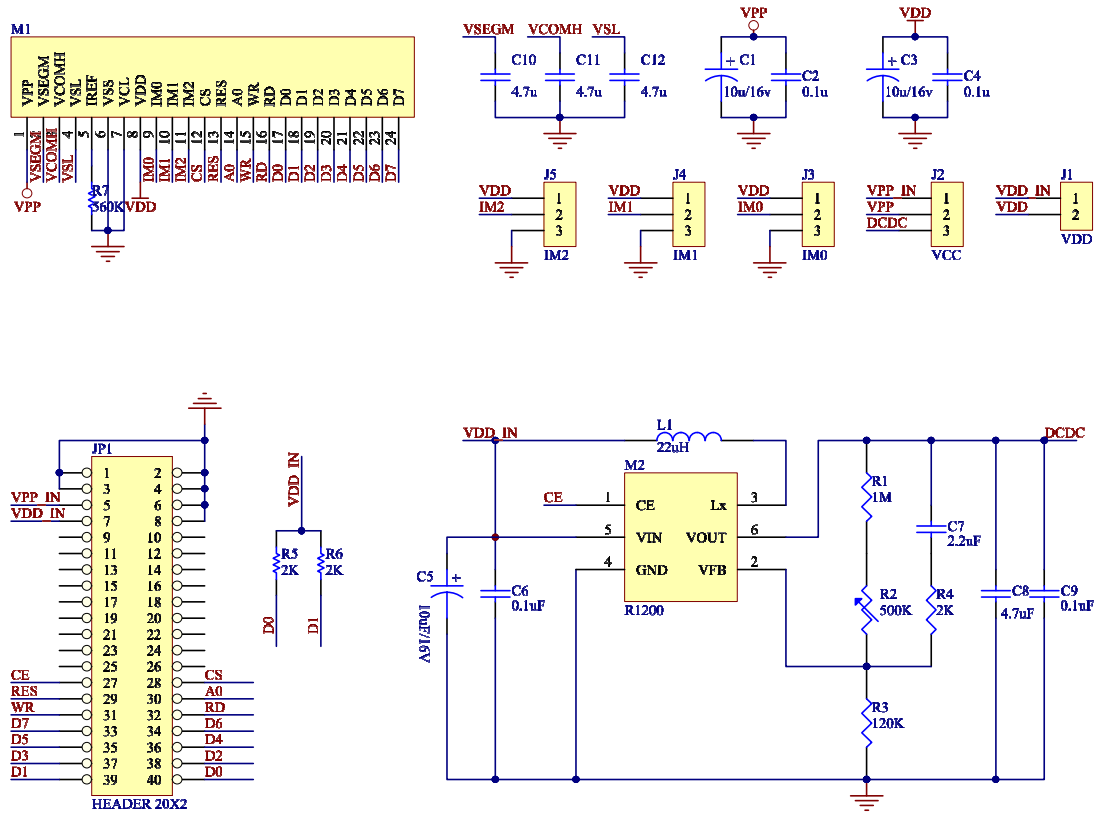


**1. Revision History**

| Version | Revision Contents | Date      |
|---------|-------------------|-----------|
| A       | New               | 2016/5/16 |
|         |                   |           |
|         |                   |           |



## 2. EVK Schematic



Note : Please refer to following table for the resistor Specification of R7.

| Module part number | Recommend value | unit |
|--------------------|-----------------|------|
| UG-5664ASWHG01     | 560K Ω          | ohm  |
|                    |                 |      |



### 3. Symbol define

#### 3.1 JP1 :

**D0~D7(SCL,SI)** : This is an 8-bit bi-directional data bus that connects to an 8-bit or 16-bit standard MPU data bus. When the serial interface is selected, then D0 serves as the serial clock input pad (SCL) and D1 serves as the serial data input pad (SI). At this time, D2 to D7 are set to high impedance. When the I2C interface is selected, then D0 serves as the serial clock input pad (SCL) and D1 serves as the serial data input pad (SDA). At this time, D2 to D7 are set to high impedance.

**A0** : This is the Data/Command control pad that determines whether the data bits are data or a command. A0 = "H": the inputs at D0 to D7 are treated as display data. A0 = "L": the inputs at D0 to D7 are transferred to the command registers. In I2C interface, this pad serves as SA0 to distinguish the different address of OLED driver.

**RES** : This pin is reset signal input (active LOW).

**CS** : This pin is chip select input (active LOW).

**WR** : This pin is the read/write control signal (active LOW).

**RD** : This pin is a MPU interface input (active LOW).

**CE** : Control DC-DC Chip Enable Pin(DC-DC Chip FOR R1200)

**VPP\_IN** : This is Power supply input for panel driving voltage. This is also the most positive power voltage supply pin. It is supplied by external voltage source.

**VDD\_IN** : Low voltage power supply. It is supplied by external voltage source.

**GND** : Power supply ground.

#### 3.2 Jump

**J2-VCC** : Jump link VPP\_IN pin and VPP pin.

**J1-VDD** : Jump link VDD\_IN pin and VDD pin.

**J3,J4,J5(IM0,IM1,IM2)** : These are the MPU interface mode select pin.

#### 4. EVK use introduction

**UG-5664GSWHG01** is COG type module, please refer to Fig1, Fig2. User can use leading wire to connect EVK with customer's system. The example shows as Fig3.

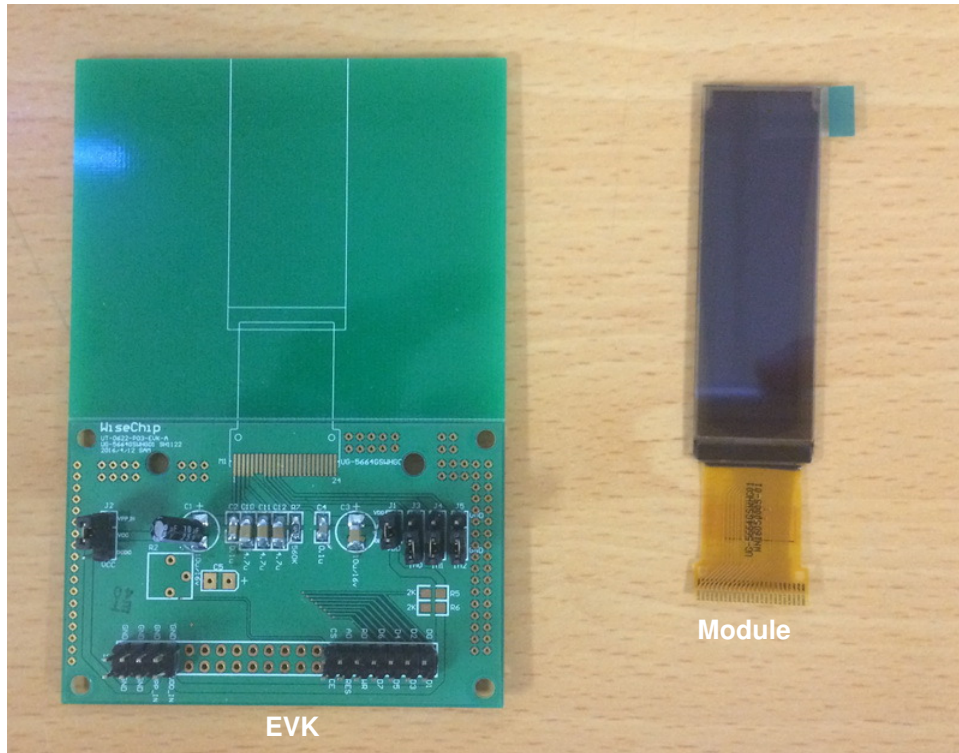


Figure 1 EVK PCB and OLED Module

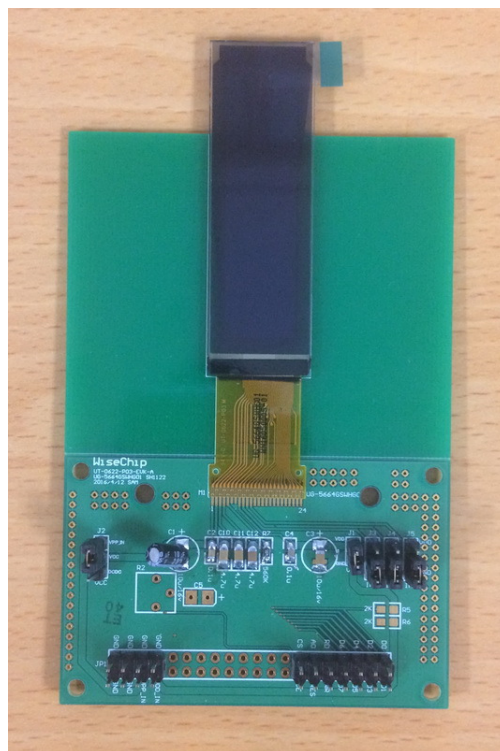
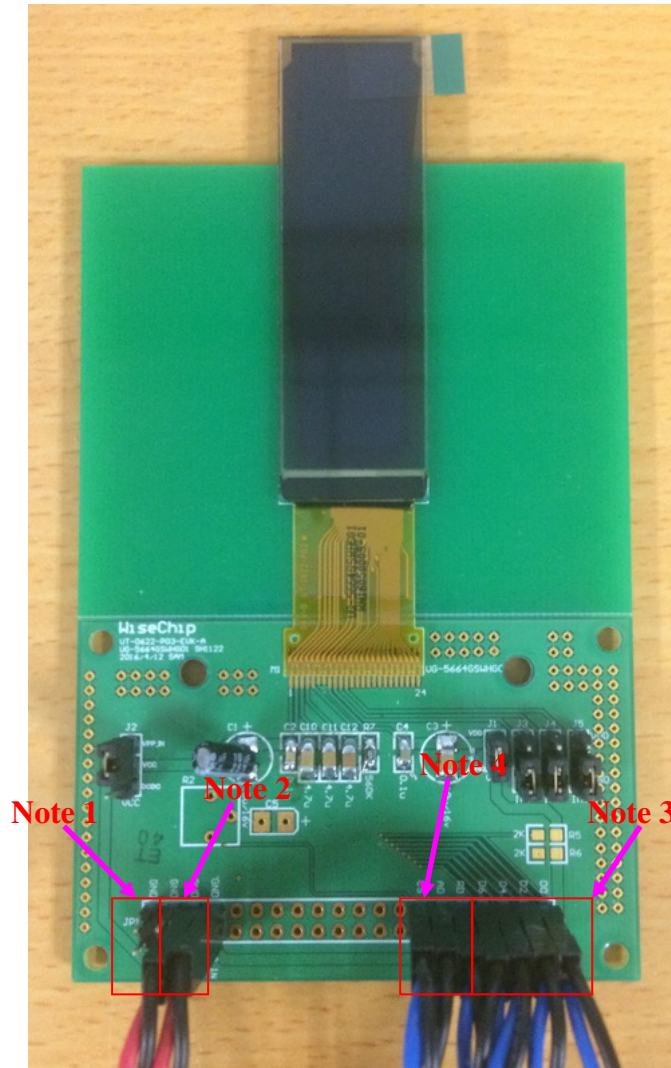


Figure 2 The combination of the module and EVK



**Figure 3 EVK with test platform**

Note 1 : This is VPP which OLED high voltage supply.

Note 2 : This is VDD which logic voltage supply.

Note 3 : Those are leading wire connect to control board. Those are data pin.(D0~D7)

Note 4 : Those are leading wire connect to control board. Those are control pin.

(CS、A0、RES、WR、RD)